SIEMENS

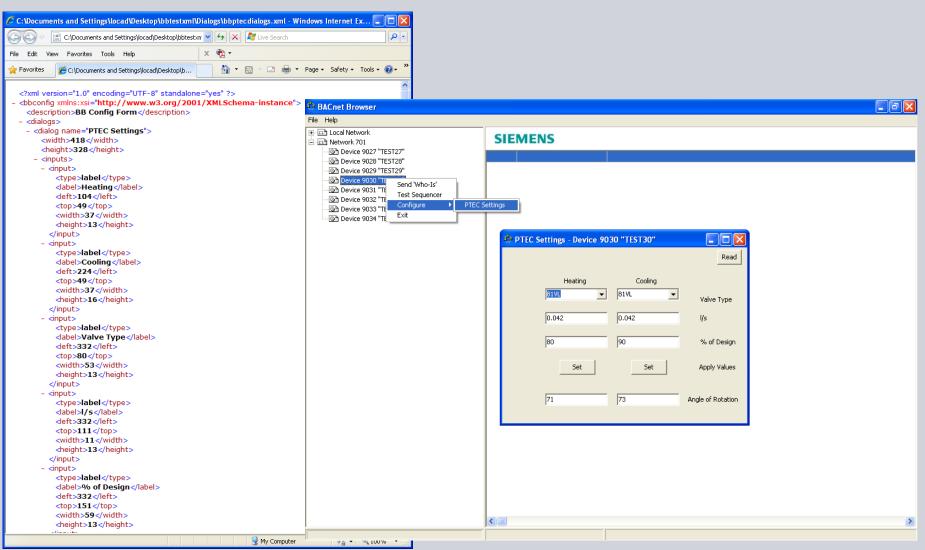


BACnet Browser

Commissioning Extensions

Energy & Sustainability

Custom Dialogs XML Import of Device Specific Dialogs



Test Sequencer Import Predefined Tests



Test sequences are engineered via Excel and exported to XML

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: 🏞	12 7	5							Over version -"1.0" encoding -"UTE_0" standologe -"ves" 25
	B		✓ fx PTEC	C test procedure	v1 (for develop	ment)			xml version="1.0" encoding="UTF-8" standalone="yes" ? - <bbtest xmlns:xsi="http://www.w3.org/2001/XMLSchema-instance"></bbtest>
	А	В	С	D	E	F	G	Н	<pre><description>PTEC test procedure v1 (for development)</description> <pre>- <steps></steps></pre></pre>
1			_						- <steps></steps>
2		PTEC te	est procedure v1 (fo	r development)					<sid>1</sid>
3									<pre><name>Start Fan</name> <read>C = 1</read></pre>
4		sid	name	read	test	write	sleep		<pre><write>4:46:85:9:8 = C</write></pre>
5		1	Start Fan	C = 1		4:46:85:9:8 = C			- <step></step>
6			FanSpd->65%	A = 3.75		1:32:85:4:8 = A			<sid>2</sid>
7			ClgVIv->0%	C = 0		1:79:85:4:8 = C			<name>FanSpd->65%</name> <read>A = 3.75</read>
8			HtqVIv->0%			1:80:85:4:8 = C	10		<write>1:32:85:4:8 = A</write>
9			HtgVlv->100%	C = 100		1:80:85:4:8 = C	1		- <step></step>
10			SupT (Os)	B = 0:15:85			10		<sid>3</sid>
11			SupT (+10s)	A = 0:15:85					<name>ClgVlv->0%</name> <read>C = 0</read>
12			*Heating*	C = 5	A>B+C				<pre><write>1:79:85:4:8 = C</write></pre>
13			ClqVlv->0%	C = 0		1:79:85:4:8 = C			- <step></step>
14		10	HtgVIv->0%			1:80:85:4:8 = C			<sid>4</sid>
15		11	Clg->100%	C = 100		1:79:85:4:8 = C	1		<name>HtgVlv->0%</name> 1:80:85:4:8 = C
16		12	SupT (Os)	B = 0:15:85			10		<sleep>10</sleep>
17		13	SupT (+10s)	A = 0:15:85					- <step></step>
18		14	*Cooling*	C = 5	A <b-c< td=""><td></td><td></td><td></td><td><sid>5</sid></td></b-c<>				<sid>5</sid>
19		15	Fan->Auto	C = 0		4:46:85:0:8 = C			<name>HtgVlv->100%</name>
20		16	FanSpd->Auto			1:32:85:0:8 = C			<read>C = 100</read> <write>1:80:85:4:8 = C</write>
21		17	ClgVlv->Auto			1:79:85:0:8 = C			<sleep>1</sleep>
22		18	HtgVl∨->Auto			1:80:85:0:8 = C			- <step></step>
23		19	RmT	A = 0:4:85					<sid>6</sid>
24			SupT	A = 0:15:85					<name>SupT (0s) </name> <read>B = 0:15:85 </read>
25			RmH	A = 0:126:85					<sleep>10</sleep>
26		22	CO2	A = 0:125:85					- <step></step>
27									<sid>7</sid>
28									<name>SupT (+10s)</name> <read>A = 0:15:85</read>
29									
30									- <step></step>
31									Done 🧧 🗸 🔩 100% 👻 🛒
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Control Products & Systems

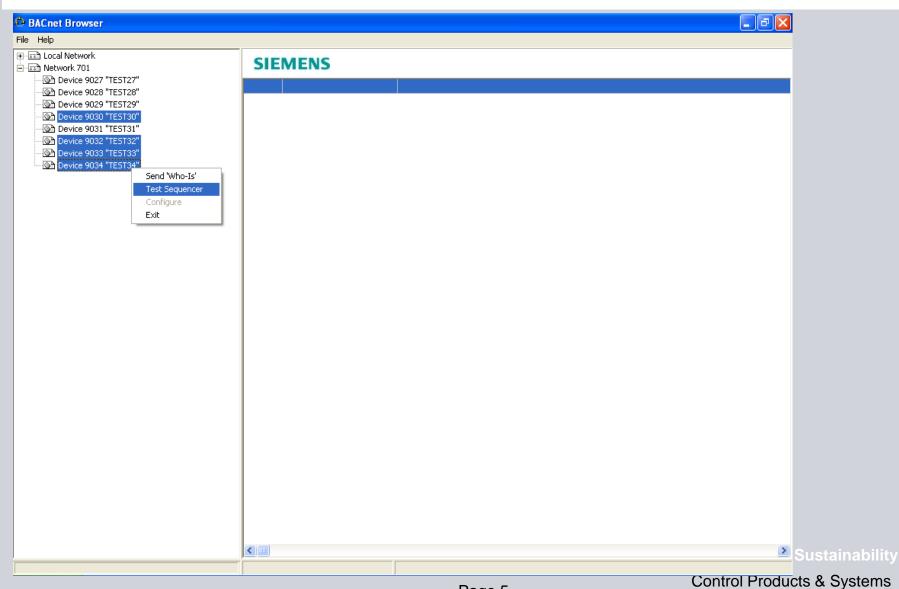
Test Sequencer Import Predefined Tests



BACnet Browser			
Help			
Settings	SIEMENIS		
Send 'Who-Is'	SIEMENS		
nd Device "TEST27"			
(port "TEST28"			
n Server "TEST29" "TEST30"			
oad Dialog Configuration			
Test Sequencer Test Configuration			
"TEST33"			
"TEST34"			
			>
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Test Sequence Device Selection





Test Sequencer Run Test

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Loaded Test	PTEC test procedu	re v1 (for developm	ent)	Stop	Write				
Name	HtgVlv->100%	SupT (0s)	SupT (+10s)	*Heating*	ClgVlv->0%	HtgVlv->0%	Clg->100%	SupT (0s)	
Device 9030 "TEST30"	ОК	30.5	30.5	Fail	ОК	ОК	ОК		
Device 9032 "TEST32"	ОК	27.1	27.1	Fail	ОК	OK			
Device 9033 "TEST33"	ОК	25.5	25.5	Fail	ОК	OK			
Device 9034 "TEST34"	ОК	27.4	27.4	Fail	OK	OK			

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Test Sequencer Export Results

Evice 9030 "TEST30" OK OK OK OK OK Solution Solution	ice 9030 "TEST30" OK					1 (101 0	evelopm	ent)		F	lun			Write	[]									
evice 9030 "TEST30" OK	ice 9030 "TEST30" OK	me	Start F	FanSpo	d ClgVlv-	HtgVlv	- HtgVlv	/- SupT (SupT (- *Heati	ir ClgVlv-	HtgVlv-		l(SupT ((*Coolin	Fan->4	FanSpd	ClgVlv-:	HtgVlv	RmT	SupT	RmH	CO2
write 9033 "TEST33" OK	ice 9033 "TEST33" OK OK OK OK OK Z.5 Z.5 Fail OK OK OK OK OK Z.4 Z.5 S0 1000 ice 9034 "TEST34" OK Z.4 Z.5 S0 1000 ice 9034 "TEST34" OK OK OK OK OK OK OK OK OK Z.4 Z.5 S0 1000 ice 9034 "TEST34" OK OK OK OK Z.4 <	vice 9030 "TEST30"																					50	1000
wice 9034 "TEST34" OK O	ice 9034 "TEST34" ОК ОК ОК ОК ОК 27.4 27.4 Fail ОК ОК 27.4 27.4 Fail ОК ОК 27.4 27.4 Fail ОК ОК <u>ок</u> 23.4 27.4 50 1000 Load Import Export Print	vice 9032 "TEST32"	ОК	бк	OK	ОК	OK	27.1	27.1	Fail	ОК	ОК	OK	27.1	27.1	Fail	ОК	ОК	ОК	ОК	23.4	27.1	50	1000
Load Import Export Print	Load Import Export Print	vice 9033 "TEST33"	ОК	ОК	OK	ОК	ОК	25.5	25.5	Fail	ОК	ОК	ОК	25.5	25.5	Fail	ОК	ОК	ОК	ОК	23.4	25.5	50	1000
		rice 9034 "TEST34"	OK	OK	ОК	OK	OK	27.4	27.4	Fail	ОК	OK	OK	27.4	27.4	Fail	OK		Load Import Export Print		23.4	27.4	50	100

Test Sequencer Table for Live Data

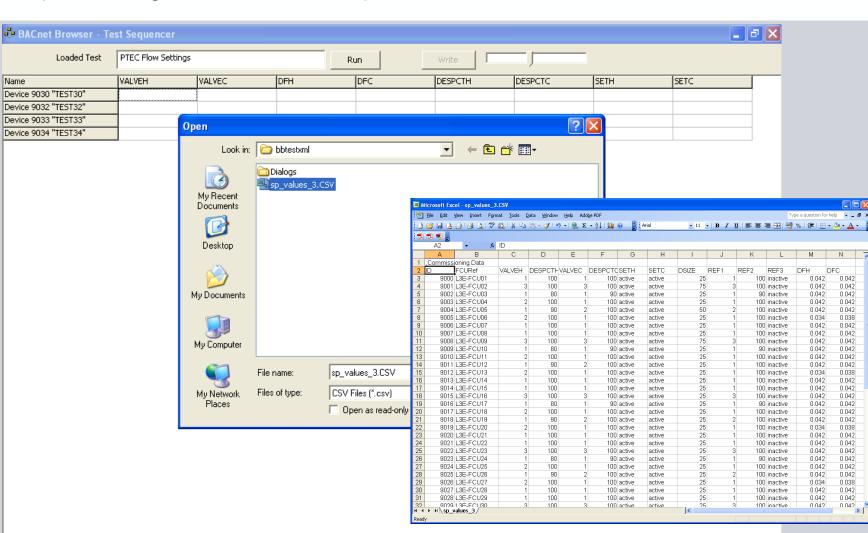
SI	Ε	Μ	Ε	Ν	S

🕹 BACnet Browser - Test Sequencer _ **d** 🗙 PTEC Flow Settings Loaded Test Write Run VALVEH VALVEC DFC Name DFH DESPCTH DESPCTC SETH SETC Device 9030 "TEST30" 1 1 0.042 0.042 80 90 inactive inactive Device 9032 "TEST32" 2 90 0.042 0.042 100 inactive inactive Device 9033 "TEST33" 1 0.034 0.038 100 100 inactive 2 inactive Device 9034 "TEST34" 100 1 0.042 0.042 100 1 inactive inactive

BACnet	WriteProperty [9034][1,1013]	
Property	present value	Index -1
Value	100	
Tag	Real	Write
Priority	Priority 8	

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Test Sequence Import Design Data – CSV Import



Sustainability



Test Sequence Import Design Data – Write to Controller

BACnet Browser - Test Sequencer												
Loaded Test	PTEC Flow Settings		R	un	Write							
Name	VALVEH	VALVEC	DFH	DFC	DESPCTH	DESPCTC	SETH	SETC				
Device 9030 "TEST30"	1	1	0.042	0.042	80	90	active	active				
Device 9032 "TEST32"	1	2	0.042	0.042	90	100	active	active				
Device 9033 "TEST33"	2	1	0.034	0.038	100	100	active	active				
Device 9034 "TEST34"	1	1	0.042	0.042	100	100	active	active				